TLCS-85 Family (NMOS)

X44

Type No.	Function	Min. Inst. Execu-tion time (µs)	Max. Supply Current (mA)	Frequency (MHz)	No. of Pins
TMP8085AP-2	Microprocessor	0.8	170	5	40
*TMP8085AHP-2	Microprocessor	0.8	135	5	40

Notes) Postfix P: Plastic standard DIP (DIP), *: Supply voltage: 4.5~5.5V; Supply voltage: 4.75~5.25V, Operating temperature: 0~70°C

TLCS-85 Family (CMOS)

X44

Type No.	Function	Standard Supply Current (mA)	Frequency (MHz)	Package (No. of pins)			
				DIP	QFP	PLCC	SOP
TMP82C51A□-2	Programmable communication interface	1.2	5	28	—	—	28
TMP82C51A□-10	Programmable communication interface	3	10	28	—	—	28
TMP82C54□-2	Programmable interval timer	3	10	24	—	—	24
TMP82C55A□-2	Programmable peripheral interface	2	5 (TRD=140ns)	40	—	—	40
TMP82C55A□-10	Programmable peripheral interface	2	10 (TRD=100ns)	40	—	—	40
TMP82C59A□-2	Programmable interrupt controller	1	8 (TRD=120ns)	28	—	—	28
TMP82C79□-2	Programmable keyboard/display interface	2	5	40	—	—	40
TMP82C37A□-5	Programmable DMA controller	5	5	40	—	—	40
TMP82C37B□-8	Programmable DMA controller	16	8	40	—	44	40
TMP82C255AN-2	Programmable peripheral interface x 2	3	5 (TRD=140ns)	64	—	—	—
TMP82C255AN-10	Programmable peripheral interface x 2	3	10 (TRD=140ns)	64	—	—	—
TMP82C265BF-2	Programmable peripheral interface x 2	3	5 (TRD=140ns)	—	80	—	—
TMP82C265BF-10	Programmable peripheral interface x 2	3	10 (TRD=100ns)	—	80	—	—

Notes) Supply voltage: 4.5~5.5V, Operating temperature: –40~85°C.
□ : Indicates package types of follows: P: Plastic standard DIP (DIP),   M: Plastic small outline package (SOP),
F: Plastic flat package (QFP),   N: Plastic shrunk DIP (SDIP),
T: Plastic leaded chip carrier (PLLC).

TLCS-Z80 ASSP Family (CMOS)

X44

Type No.	Function	Min. Inst. Execu-tion time (µs)	Max. Supply Current (mA)	Package (No. of pins)		
				DIP	QFP	PLCC
TMPZ84C011BF-6/BF-8	CPU + CTC + CGC + I/O (8 x 5)	0.6/0.5	22/31	—	100	—
TMPZ84C013AT-6/AT-8/AT-10	CPU + SIO + CTC + CGC + Watchdog timer	0.6/0.5/0.4	22/31/40	—	—	84
TMPZ84C015BF-6/BF-8/BF-10/BF-12	CPU + SIO + CTC + PIO + CGC + Watchdog timer	0.6/0.5/0.4/0.3	25/35/45/55	—	100	—
TMPZ84C112AN-6/AF-6	CPU + 256 byte RAM + I/O (8 x 2, 5 x 1)	0.6	15	64	64	—
TMPZ84C810AF-6/AF-8/AF-10	CPU+SIO+CTC+CGC+DMA+I/O(8x1)+OUTPORT(5x1)MMU+WDT	0.6/0.5/0.4	25/35/45	—	100	—
TMPZ84C711AF-6/AF-8	CCITT spec. ISDN basic interface control processor	0.6/0.5	15/20	—	144	—

Notes) *: Operating temperature: –40~85°C; Supply voltage: 4.5~5.5V, However, 8MHz products except CPU provide operating
temperature of –10 to 70°C and supply voltage 4.75 to 5.25V.
Postfix F: Plastic flat package (QFP), T: Plastic leaded chip carrier (PLCC)
N: Plastic shrunk DIP (SDIP), G: Ceramic Flat package (QFC)

TLCS-Z80 Family (CMOS)

X44

Type No.	Function	Min. Inst. Execu-tion time (µs)	Max. Supply Current (mA)	Package (No. of pins)			
				DIP	QFP	PLCC	SOP
TMPZ84C00A□-6	Microprocessor (6MHz)	0.6	15	40	—	44	40
TMPZ84C00A□-8	Microprocessor (8MHz)	0.5	20	40	—	44	40
TMPZ84C00A□-10	Microprocessor (10MHz)	0.4	25	40	—	44	40
TMPZ84C01F	CPU + CGC (4MHz)	1.0	15	—	44	—	—
TMPZ84C02AF-6	CPU + CGC (6MHz)	0.6	20	—	44	—	—
TMPZ84C10A□-6	Direct memory access controller (6MHz)		6	40	—	44	40
TMPZ84C20A□-6/20A□-8	Parallel I/O controller (6/8MHz)		3/4	40	—	44	40
TMPZ84C30A□-6/30A□-8	Counter/timer circuits (6/8MHz)		4/5	28	—	44	28
TMPZ84C40A□-6/40A□-8	Serial I/O controller (6/8MHz)		4/5	40	—	—	40
TMPZ84C41A□-6/41A□-8	Serial I/O controller (6/8MHz)		4/5	40	—	—	40
TMPZ84C42A□-6/42A□-8	Serial I/O controller (6/8MHz)		4/5	40	—	—	40
TMPZ84C43AF-6/43AF-8	Serial I/O controller (6/8MHz)		4/5	—	44	—	—
TMPZ84C44AT-6/44AT-8	Serial I/O controller (6/8MHz)		4/5	—	—	44	—
TMPZ84C61AP-6	Clock generator/controller (6MHz)		3	16	—	—	—

Notes) Operating temperature: –40~85°C; Supply voltage: 4.5~5.5V, However, 8MHz products except CPU provide operating temperature of –10 to 70°C and supply
voltage 4.75 to 5.25V. Z80 is a trade mark of Zilog Corp., U.S.A.
□: Indicates package types of follows: P: Plastic standard DIP (DIP),   F: Plastic flat package (QFP),
T: Plastic leaded chip carrier (PLCC),   M: Plastic small outline package (SOP).